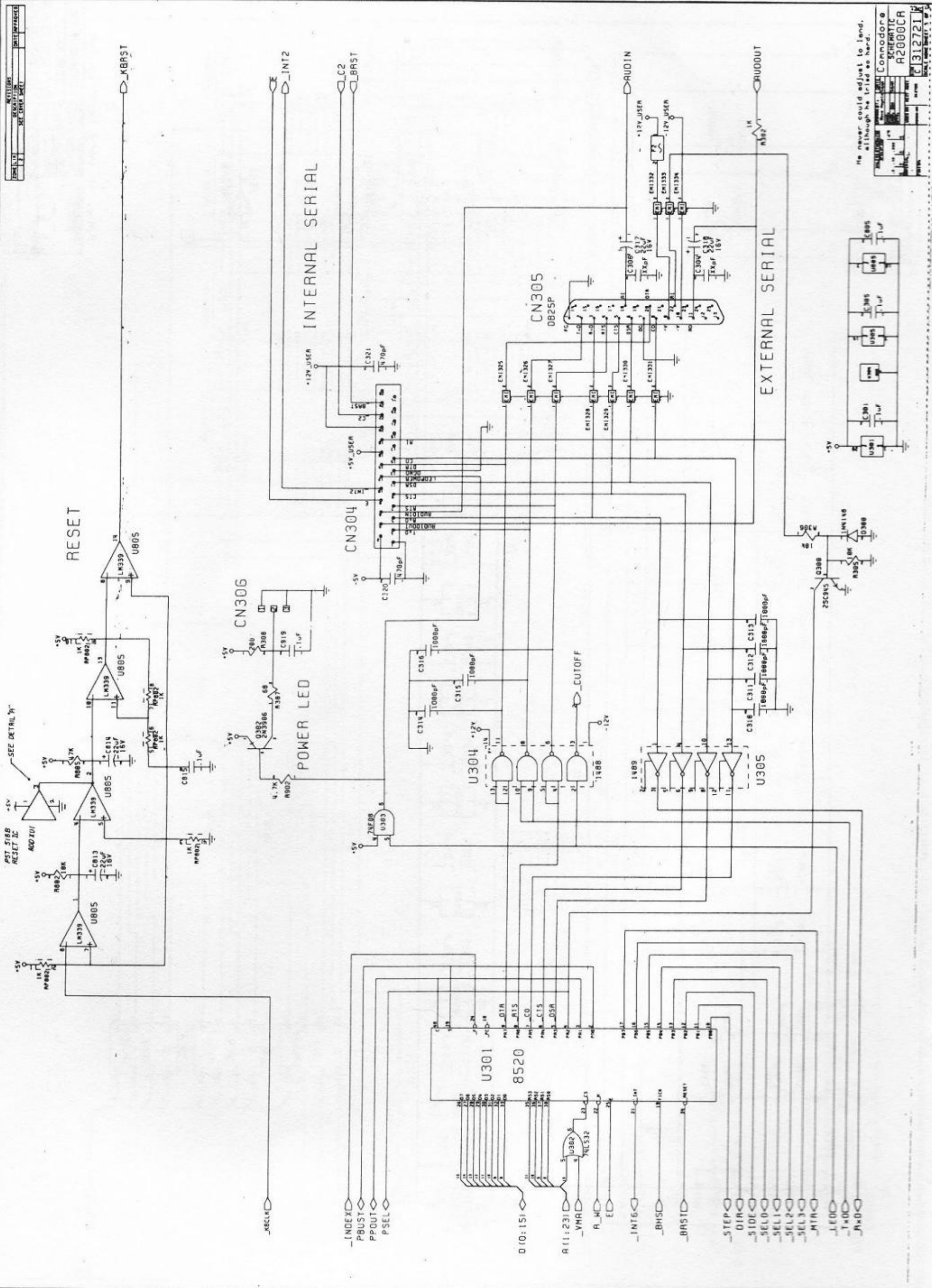
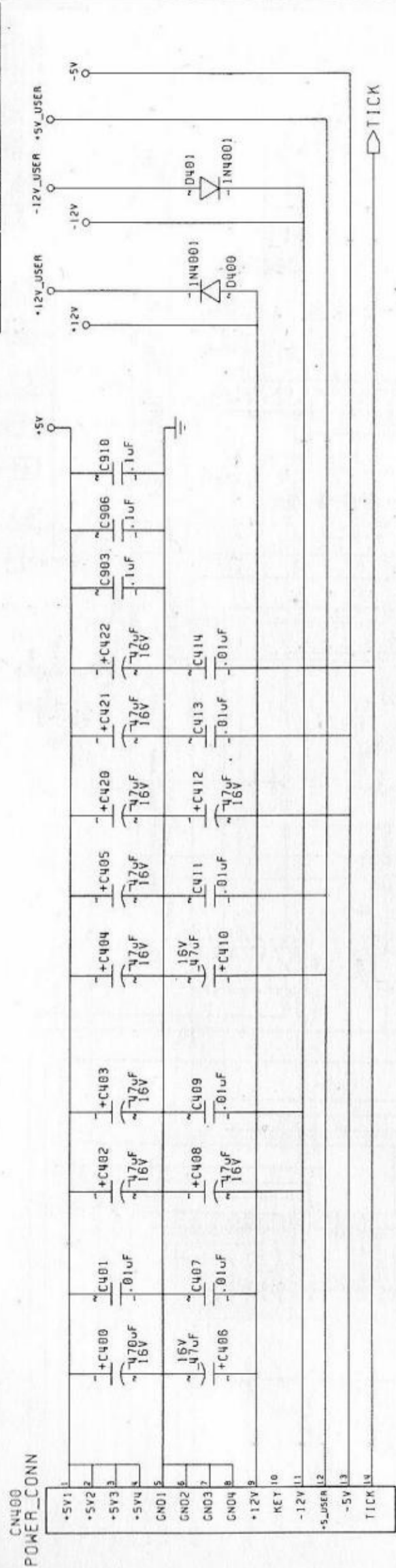


I'm pickin' up good vibrations...

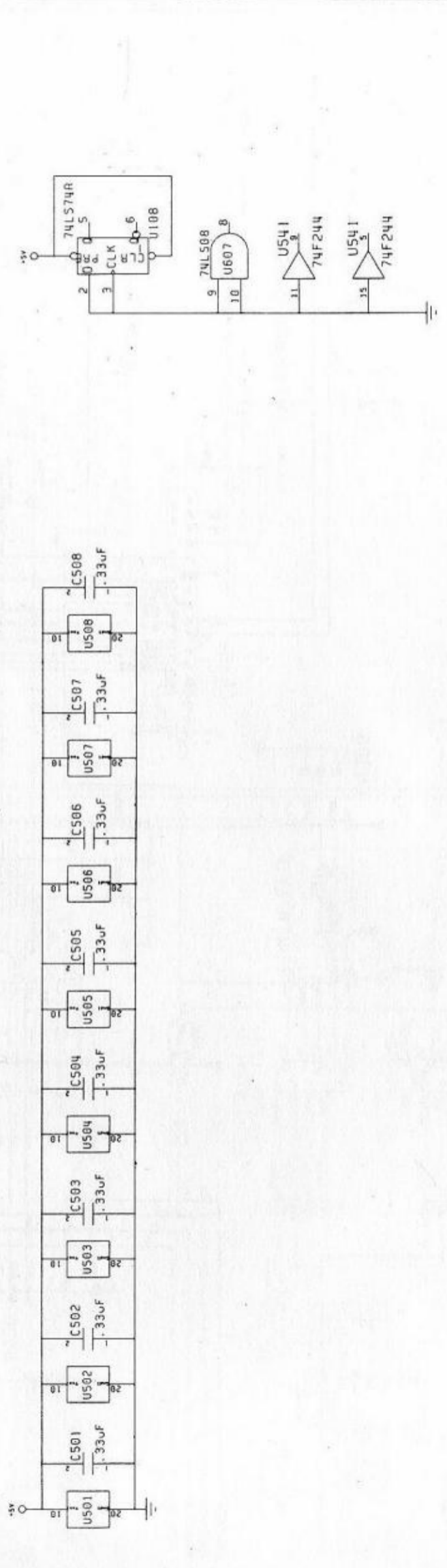
Connodore
SCHEMATIC
A2000CR
C31272



REV	DESCRIPTION	DATE	APPROVED
1	SEE COVER SHEET		
2			



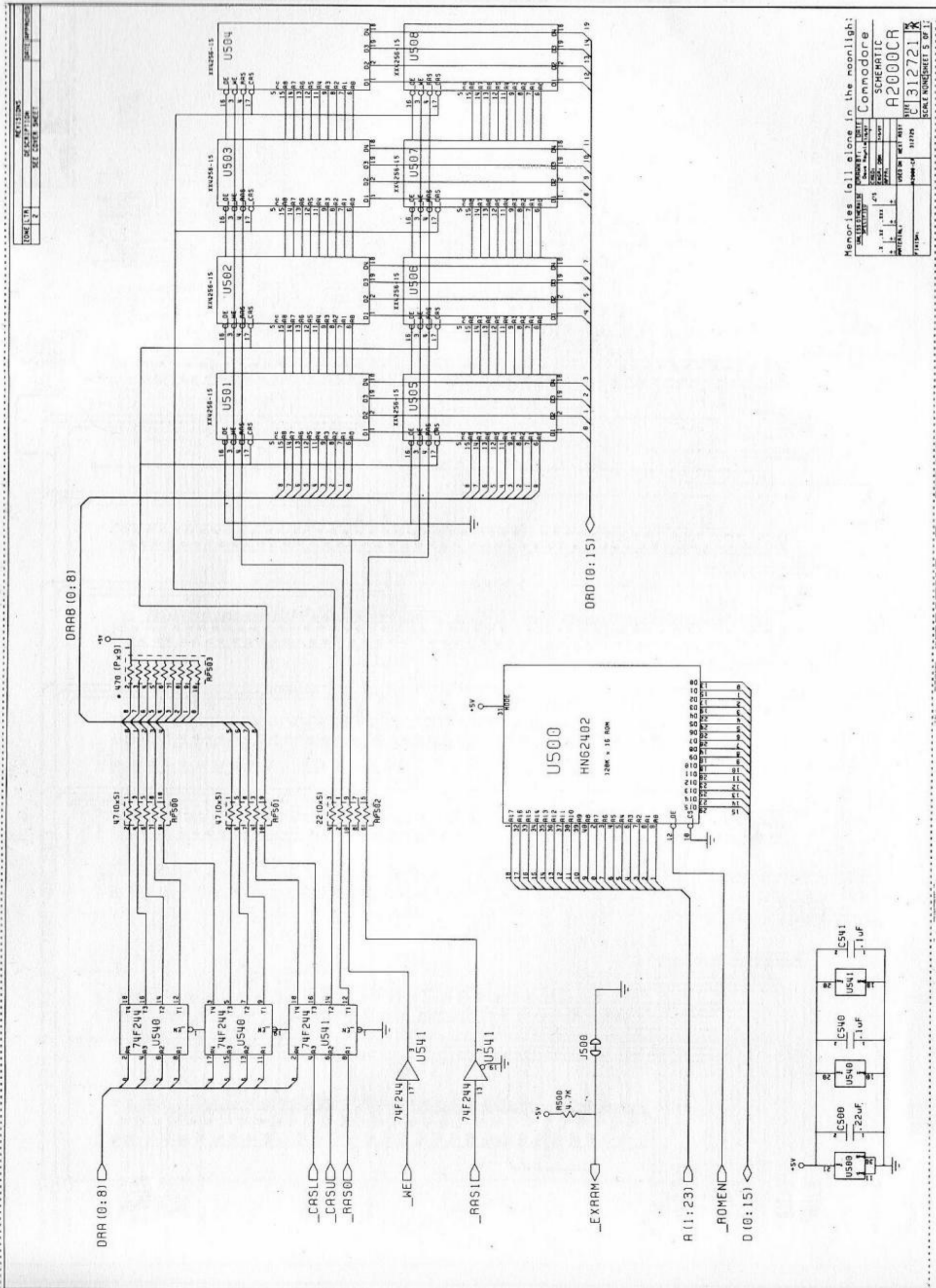
SPARES



Baby, we were born to run...

DESIGNER	DATE	COMPANY
...	...	Commodore

SCHEMATIC
A2000CR
C1312721
SCALE NONE SHEET N OF 12

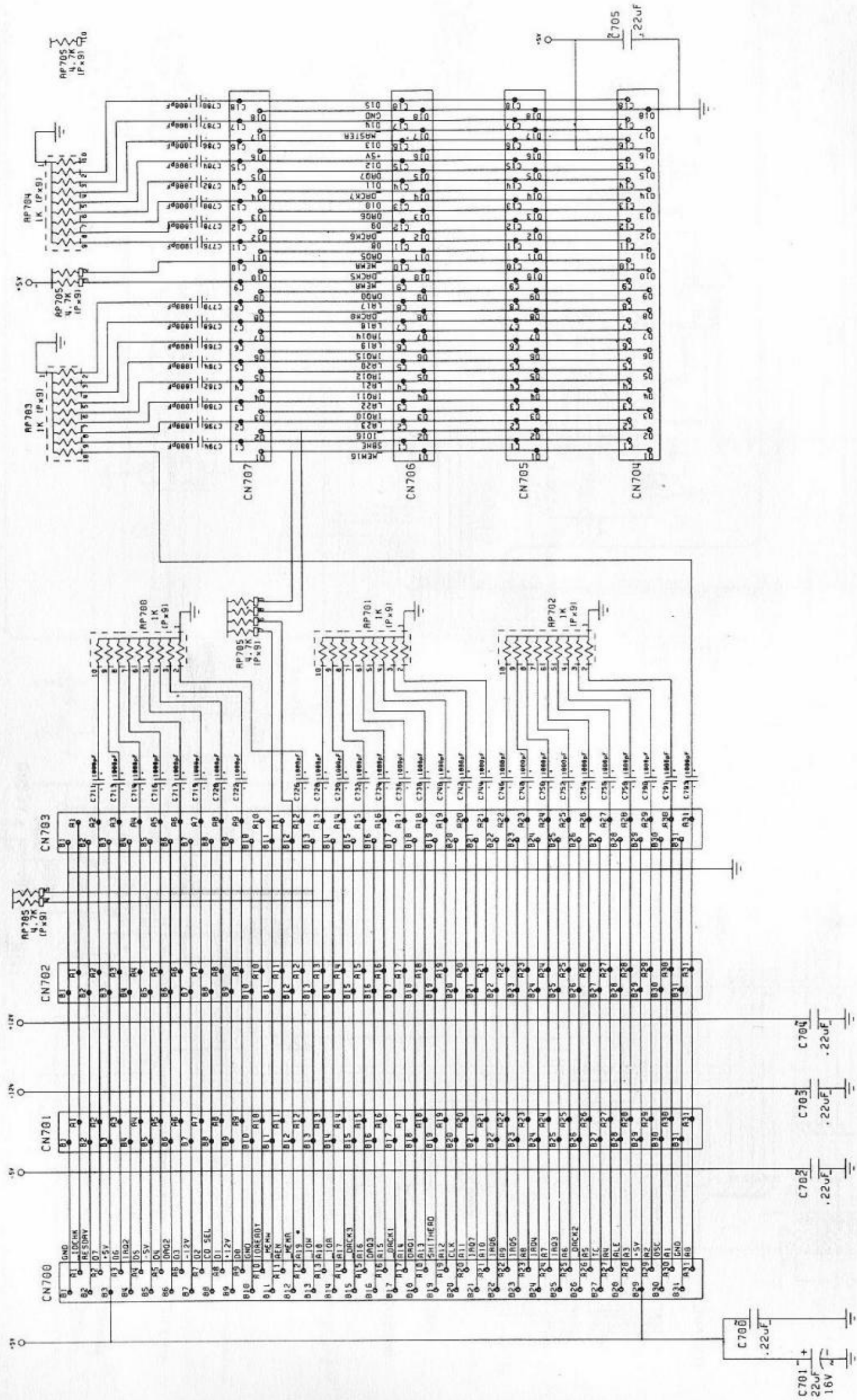


Memories fall alone in the moonlight.
UNLESS OTHERWISE INDICATED, ALL PARTS ARE COMMERCIAL GRADE.
DATE: 12/15/83
DRAWN BY: J. J. J.
CHECKED BY: J. J. J.
PARTIAL: 1
UNIT: 1
SCALE: 1:1
SHEET: 5 OF 11
PROJECT: COMMODORE A2000CR
DATE: 12/15/83
DRAWN BY: J. J. J.
CHECKED BY: J. J. J.
PARTIAL: 1
UNIT: 1
SCALE: 1:1
SHEET: 5 OF 11

REVISIONS	DATE APPROVED
DESCRIPTION	DATE APPROVED
ZONE LTR	2
SEE COVER SHEET	

IBM 16 BIT EXTENDED BUS

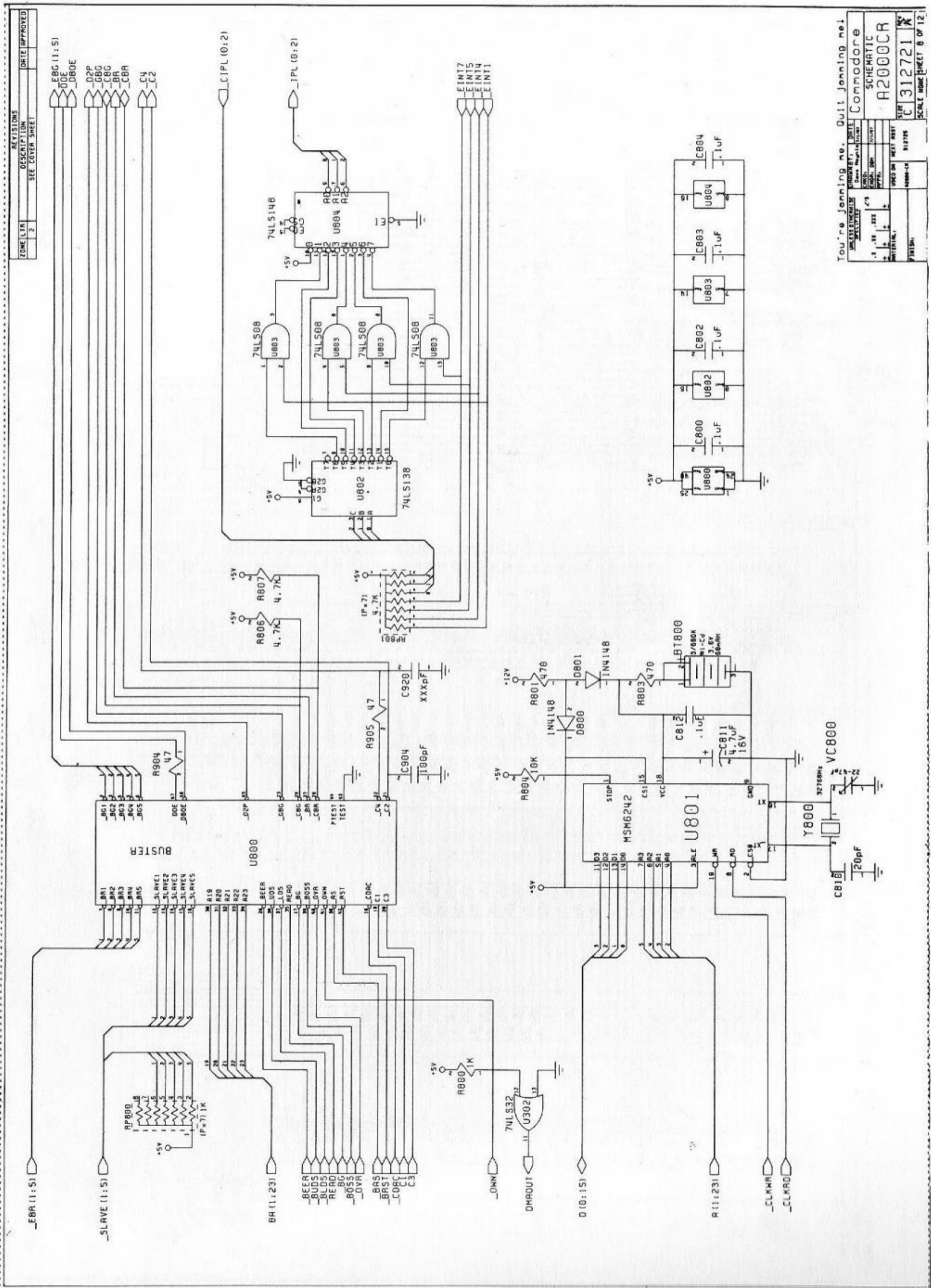
IBM 8 BIT EXPANSION BUS



I will in this place, where the sun never shines

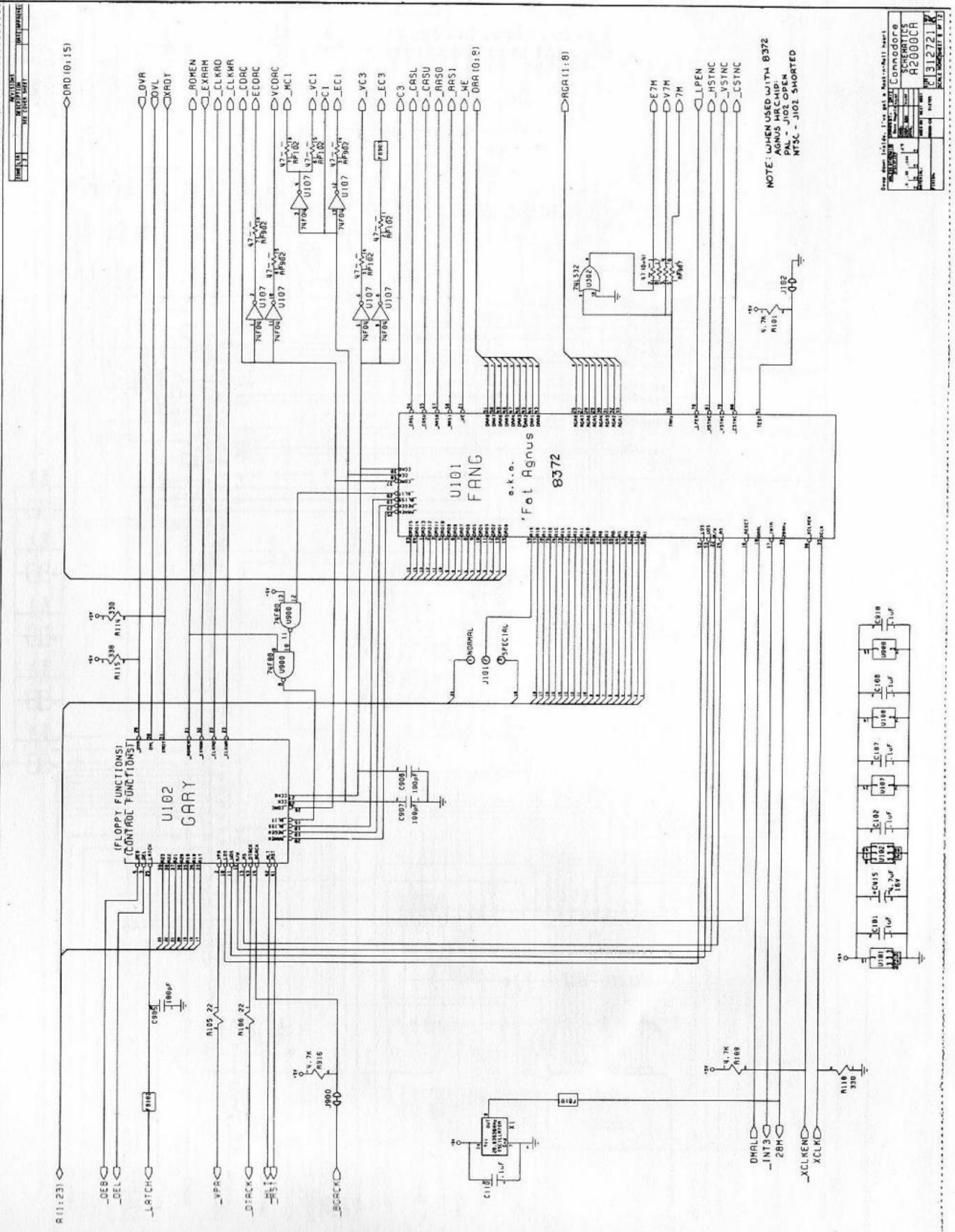
DATE	DESIGNER	DATE	SCALE
1975	AP700	1975	1:1
1975	AP701	1975	1:1
1975	AP702	1975	1:1
1975	AP703	1975	1:1
1975	AP704	1975	1:1
1975	AP705	1975	1:1
1975	AP706	1975	1:1
1975	AP707	1975	1:1
1975	AP708	1975	1:1
1975	AP709	1975	1:1
1975	AP710	1975	1:1
1975	AP711	1975	1:1
1975	AP712	1975	1:1
1975	AP713	1975	1:1
1975	AP714	1975	1:1
1975	AP715	1975	1:1
1975	AP716	1975	1:1
1975	AP717	1975	1:1
1975	AP718	1975	1:1
1975	AP719	1975	1:1
1975	AP720	1975	1:1
1975	AP721	1975	1:1
1975	AP722	1975	1:1
1975	AP723	1975	1:1
1975	AP724	1975	1:1
1975	AP725	1975	1:1
1975	AP726	1975	1:1
1975	AP727	1975	1:1
1975	AP728	1975	1:1
1975	AP729	1975	1:1
1975	AP730	1975	1:1
1975	AP731	1975	1:1

Commodore
SCHEMATIC
A2000CR
SIZE 11x17
SCALE NONE SHEET 7 OF 12



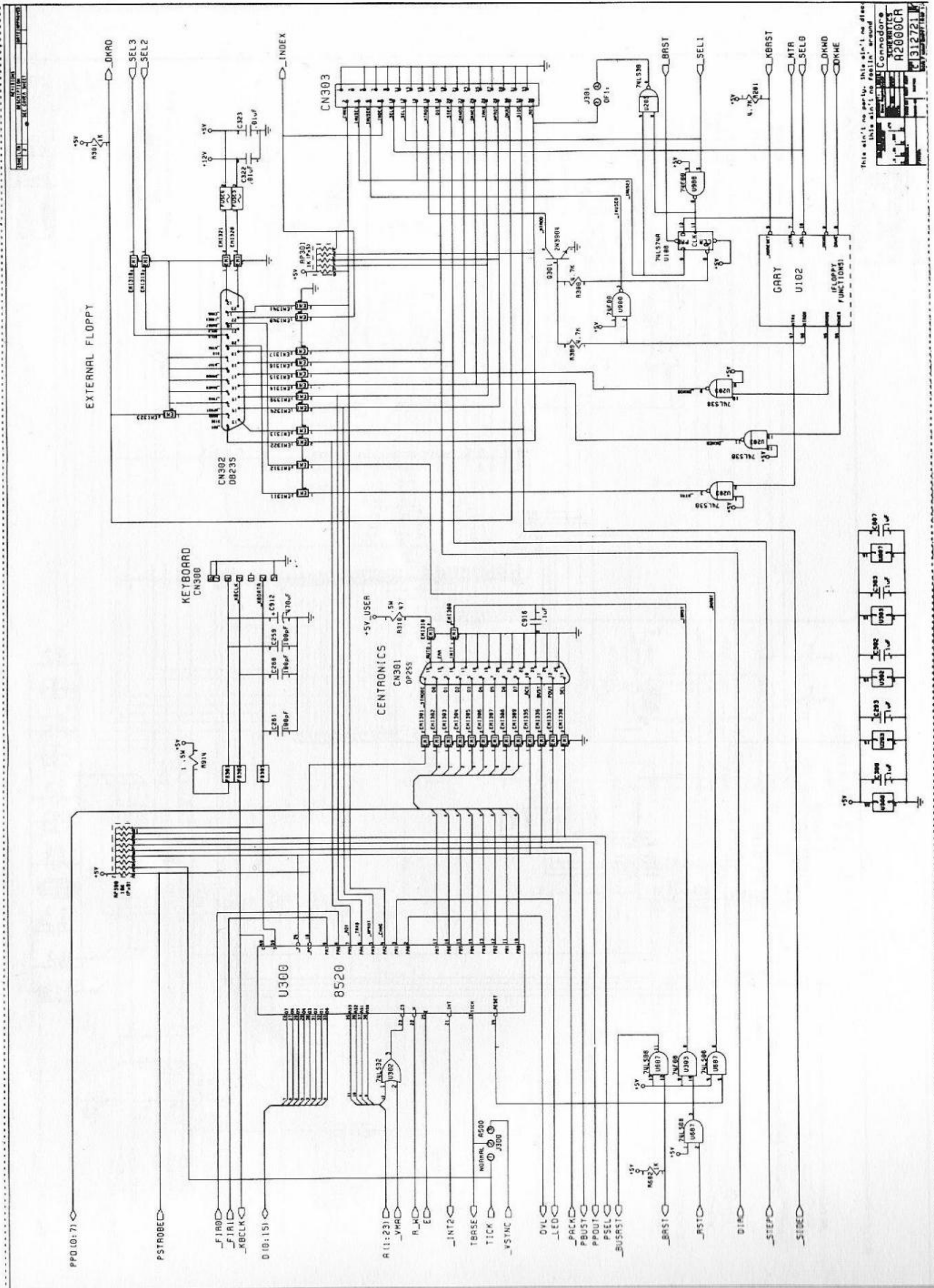
You're Jannning no. Gull Jannning net

DATE	11/14/82
DESCRIPTION	Commodore
REVISION	1
DATE	11/14/82
DESCRIPTION	SCHEMATIC
REVISION	1
DATE	11/14/82
DESCRIPTION	A2000CR
SCALE	1:1
SHEET NO.	8 OF 12



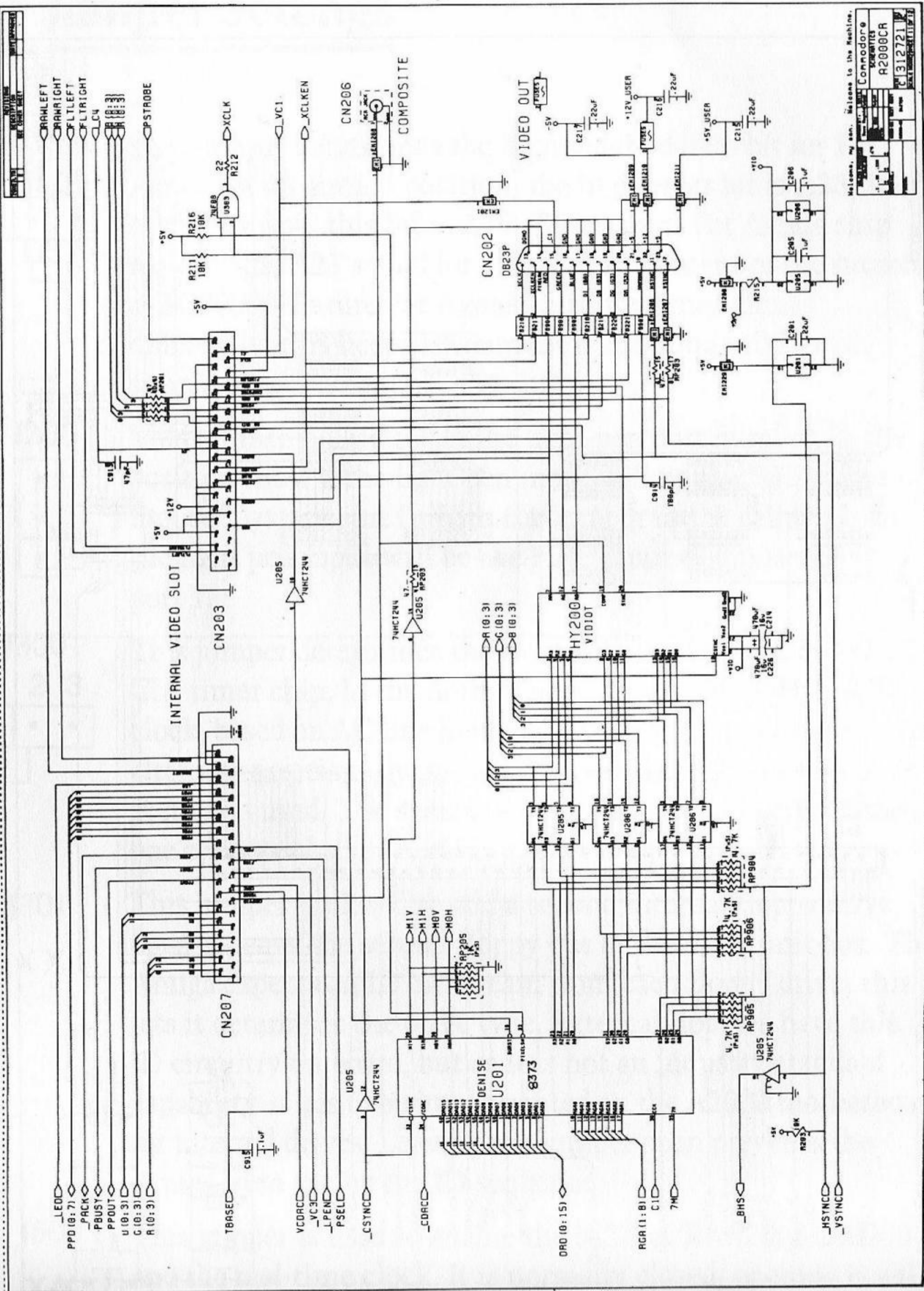
DATE	11/15/88
DESIGNED BY	CONNOR
CHECKED BY	CONNOR
APPROVED BY	CONNOR
SCALE	1:1
SHEET NO.	11
TOTAL SHEETS	12
PROJECT	312721

DATE: 11/15/88
DESIGNED BY: CONNOR
CHECKED BY: CONNOR
APPROVED BY: CONNOR
SCALE: 1:1
SHEET NO.: 11
TOTAL SHEETS: 12
PROJECT: 312721

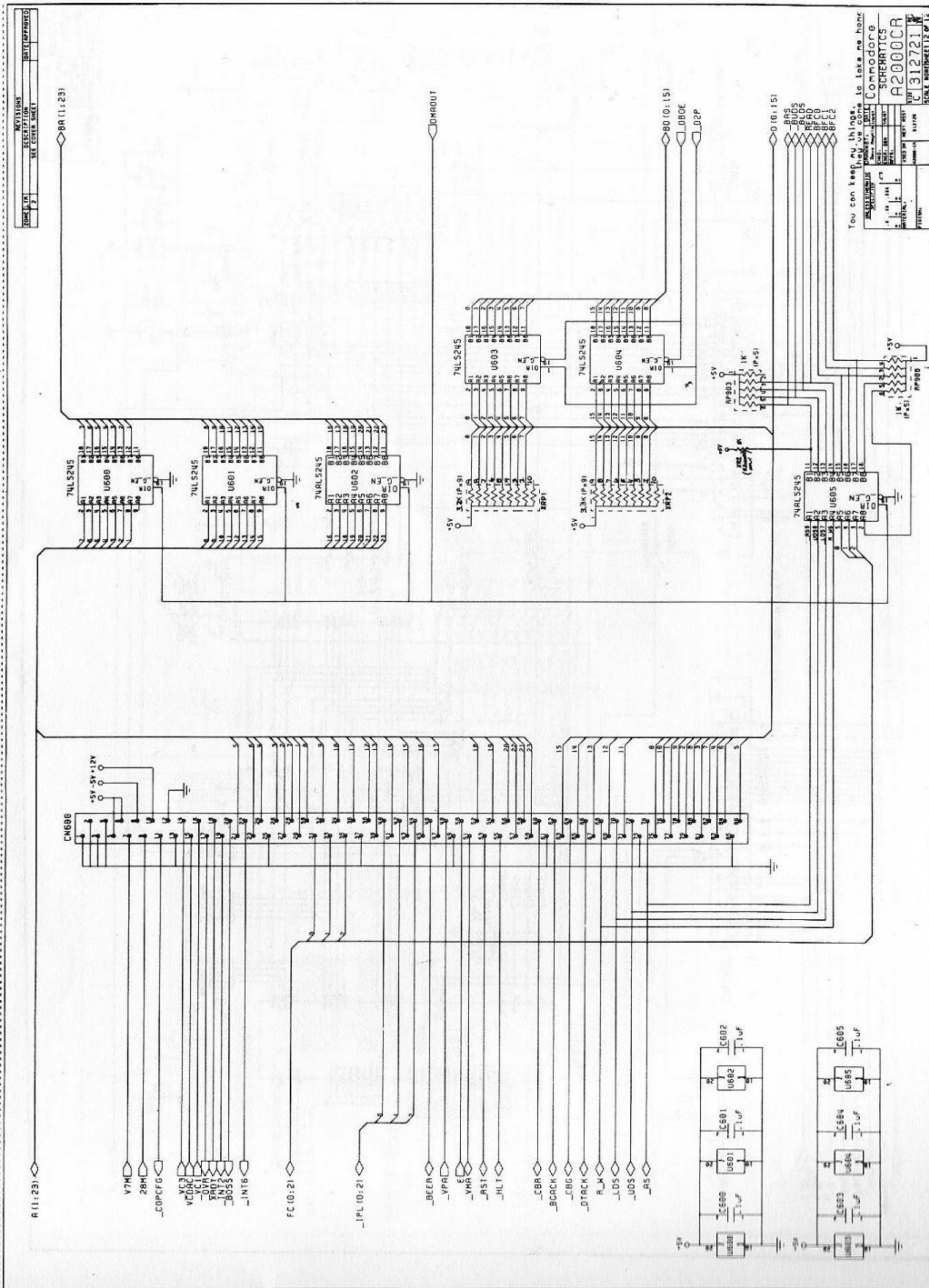


This ain't no party, this ain't no disco,
 this ain't no funk 'n' roll,
 this ain't nothin' around here.

Commodore
 6400CR
 6312721



Commodore
A2000CR
312721



REV	DESCRIPTION	DATE	APPROVED
1	SEE CONVA SHEET		

You can keep my things, they've come to take me home

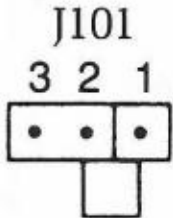
Commodore
SCHEMATICS
A2000CR
C312721

DESIGNED BY	DATE
DRAWN BY	DATE
CHECKED BY	DATE
APPROVED BY	DATE

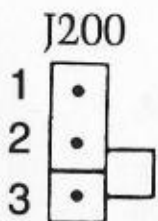
SCALE: NOMINAL 12 OF 12

Appendix E

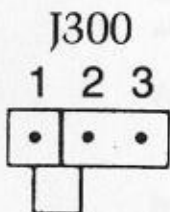
Jumper Settings



This jumper determines the high-order address bit for Fat Agnus. In its normal position, the high-order bit is A23; in its other position, this bit is A19. The current Fat Agnus chip requires the A23 signal for proper management of the memory at \$C00000. Future Fat Agnus chips may map things differently; this keeps the current board compatible with simple enhancements to the chip set.



This jumper is used to set the light-pen port number. In the position shown, the light pen input will be the FIRE input of mouse/joystick port 0. With the jumper in the other position, the light pen input will be the FIRE input of mouse/joystick port 1.



This jumper determines the time base used for the 50/60Hz CIA timer chip. In the normal position, the 50/60Hz TICK clock, based on AC line frequency, is used as a time base. In the alternate position, the vertical sync pulse from the video section is used. The system will not operate properly without one of these clocks.

J301
X X

This jumper is closed to add a second internal floppy drive, open to leave the second floppy out of the main unit box. The Amiga expects an ID bit stream from each floppy drive; this lets it determine the drive type. External floppies have this ID circuitry on board, but as it is not an industry standard capability, it has to be implemented on the A2000 motherboard for internal drives. Leaving the jumper open prevents the Amiga from seeing the ID sequence.

J500
X-X

This jumper is used to enable the 512K of RAM at \$C00000 and the real-time clock. It is normally closed; opening it will disable both this extra RAM and the real-time clock.