

SNES Front Panel Daughter Board Pinout by [Jonathon W. Donaldson \(jwdonal\)](#) (special thanks to [Martin Korth \(nocash\)](#))

Designator	Name	Desc	Type	Owner	Show	Number	Name
1	VCC	Supply	Power	1	True	True	VCC
2	JPIO6	NC	Passive	1	True	True	JPIO6
3	JPIO7	NC/Lightpen	Passive	1	True	True	JPIO7
4	4017.D0	Joypad 2 Serial Data In (from Joypad)	Passive	1	True	True	4017.D0
5	4017.D1	Joypad 4 Serial Data In (from Joypad)	Passive	1	True	True	4017.D1
6	4016.D0	Joypad 2 Serial Data In (from Joypad)	Passive	1	True	True	4016.D0
7	4016.D1	Joypad 1 Serial Data In (from Joypad)	Passive	1	True	True	4016.D1
8	JPCLK1	Joypad 1/3 Clock Pulse (to Joypad)	Passive	1	True	True	JPCLK1
9	JPCLK2	Joypad 2/4 Clock Pulse (to Joypad)	Passive	1	True	True	JPCLK2
10	JPSTR	Joypad Strobe (to Joypad)	Passive	1	True	True	JPSTR
11	GND	Supply	Power	1	True	True	GND