



NOTE: VCC = 5V VS = 5V

NOTE: 1st revision SNES has two LM2904 (Dual Op-Amps) chips instead of one LM224 (Quad Op-Amp). One 204 chip is on the SNEV-SO-DND daughterboard (for the pre-amplifier) and one is on the motherboard (for the post-amplifier).

A later version of this schematic can be found at the following URLs:
<http://www.foxnet.net/~dgar>
<http://dolphinemu.com/wiki/wiki.php?id=SNES%2F7540%2FSNES%2Fpersonal%2Fschematic%2Fschematic%2F>
<http://forums.audionet.com/viewtopic.php?p=15283-10470>
<http://www.superintend.com/>

SNE Super Nintendo (SNES) Revision 2 Motherboard (or Revision 1 SNEV-SO-DND connector)			
Revision: Rev02 (SNES CPU: CPM101) (SNEV CPU: CPM101) (SNEV CPU: CPM101)			
Rev	Number	Revision	Count
AI			1.5
File:	3/24/2011		
File:	C:\Program Files\Autodesk\Inventor\Projects\SNES\SNES-PCB-01.dwg		